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09/608.507

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Application No.

Filing Date

June 30, 2000

Group Act Unit

2180

Examiner Name _____

Thai, T.

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AUG 9

2002
per 2100

ENCLOSURES *(check all that apply)*

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<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input checked="" type="checkbox"/> Amendment / Response <ul style="list-style-type: none"> <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/ Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53		
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Firm or Individual name	Sanjeet K. Dutta, Reg. No. 46,145 BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
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page
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Attorney Docket No. 42390P8918

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re of Application of:

Salvador Palanca, et al.

Application No.: 09/608,507

Filing Date: June 30, 2000

For: METHOD AND APPARATUS FOR CACHE
REPLACEMENT FOR A MULTIPLE VARIABLE-
WAY ASSOCIATIVE CACHE

Examiner: Thai, T.

Art Group: 2186

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Technology Center 2100

Commissioner for Patents
Washington, D.C. 20231

AMENDMENT

Sir:

In response to the Office Action of May 3, 2002, please enter the following amendments.
Please cancel claims 17-21.

In the Claims

Sub
B1

1. A method comprising:
partitioning a cache array dynamically based upon requests for memory from an integrated device having a plurality of processors.
2. The method as claimed in claim 1, further comprising subdividing one or more ways within the cache array.

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